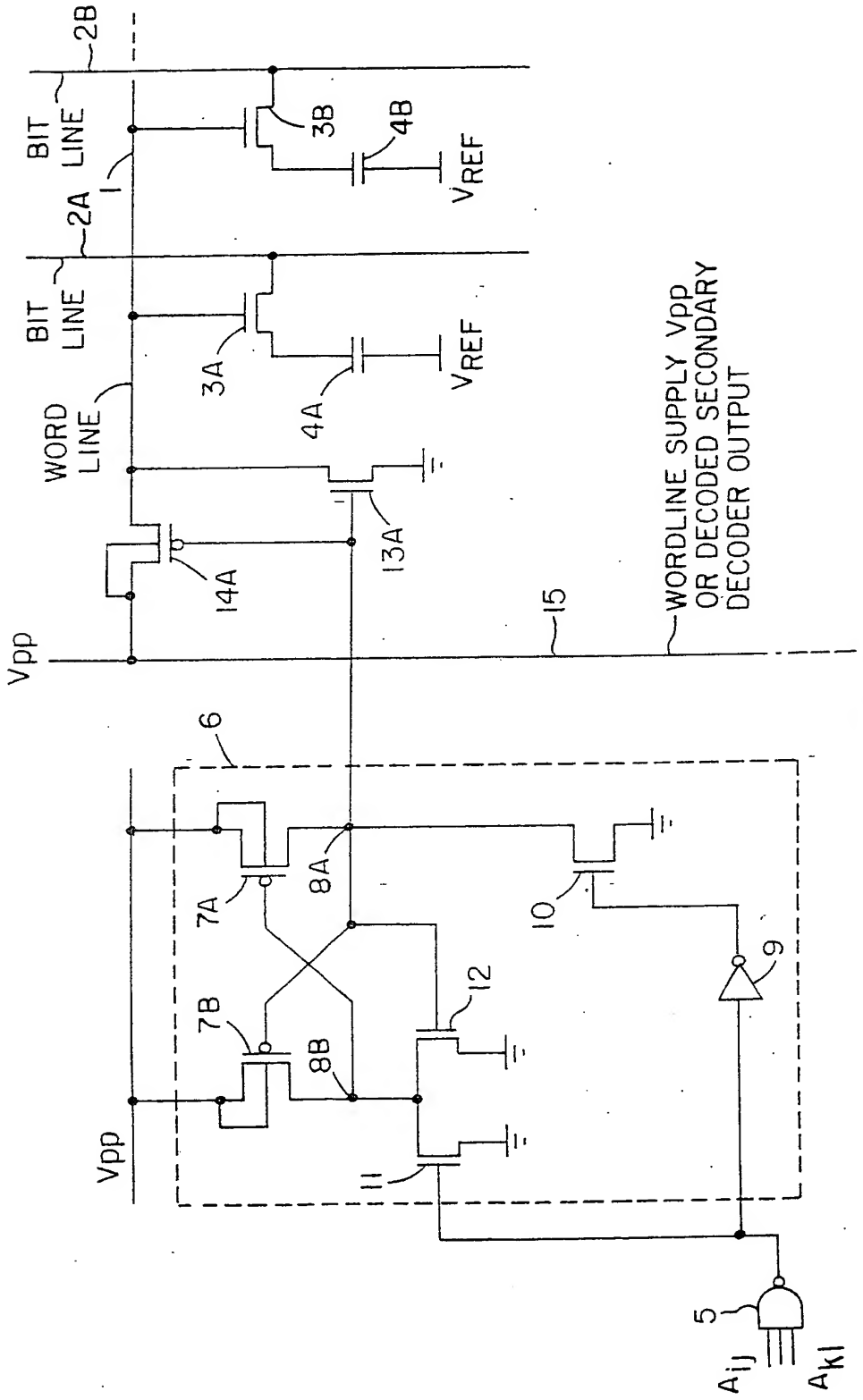
2B
BIT LINE
1
2A
BIT LINE
WORD LINE
3B
4B
V_{REF}
3A
4A
V_{REF}
13A
14A
15
WORDLINE SUPPLY Vpp OR DECODED SECONDARY DECODER OUTPUT
Vpp
6
7A
8A
10
9
12
7B
8B
Vpp
11
5
A_{ij}
A_{kl}

FIG. 1